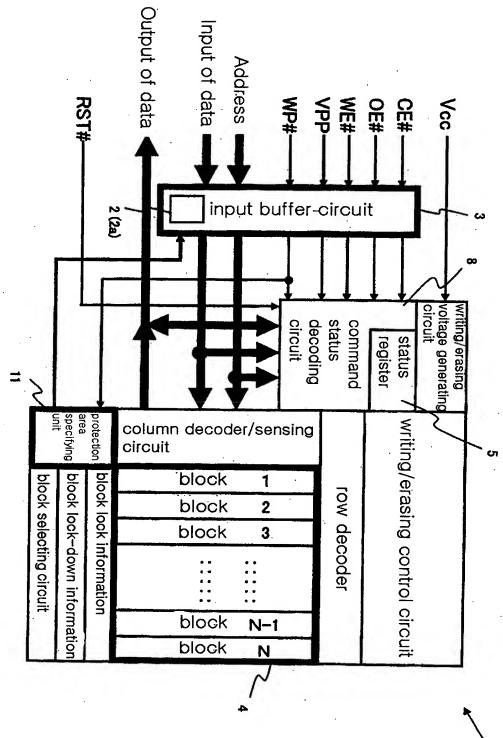


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Title: ERRONEOUS OPERATION PREVENTING CIRCUIT OF NON-VOLATILE MEMORY DEVICE

REPLACEMENT SHEET

Docket No.: 577642000100



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FIG. 2

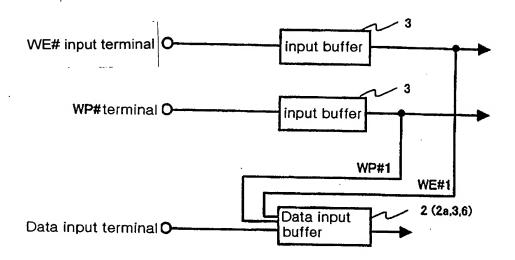
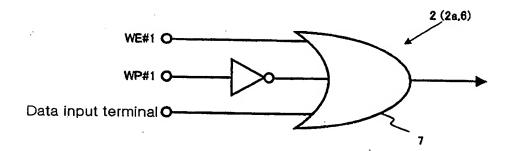


FIG. 3



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FIG. 4

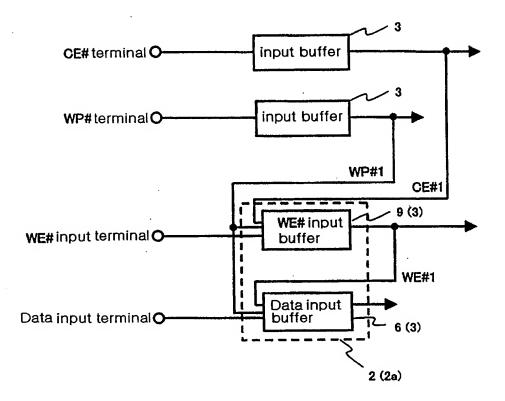
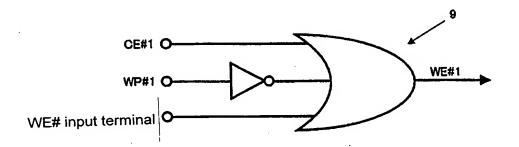


FIG. 5



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FIG. 6

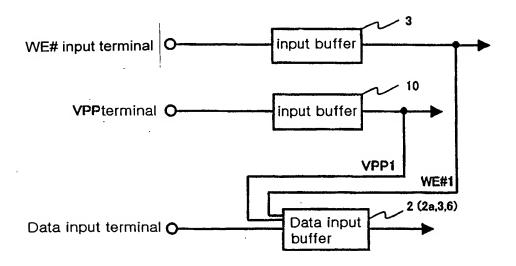
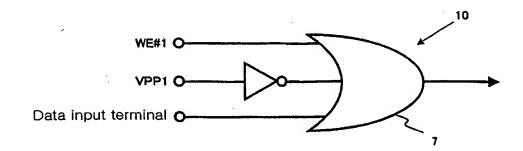


FIG. 7



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FIG. 8

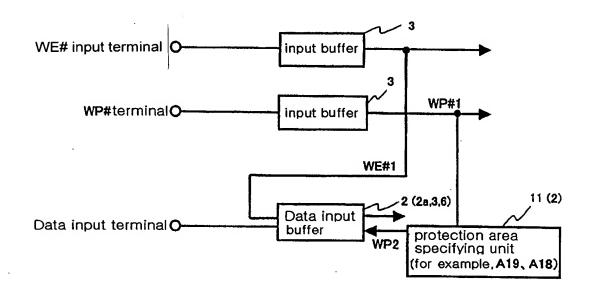
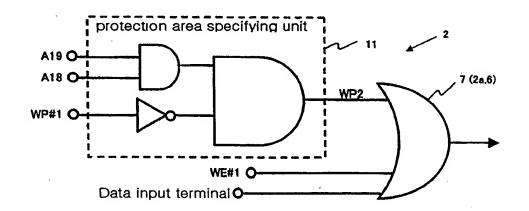


FIG. 9

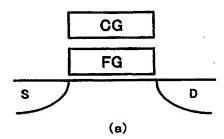


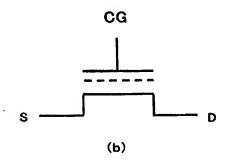
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Inventor: Katsumi FUKUMOTO
Title: ERRONEOUS OPERATION PREVENTING CIRCUIT OF
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FIG. 10

Prior Art





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REPLACEMENT SHEET

FIG. 11

**Prior Art** 

